

100

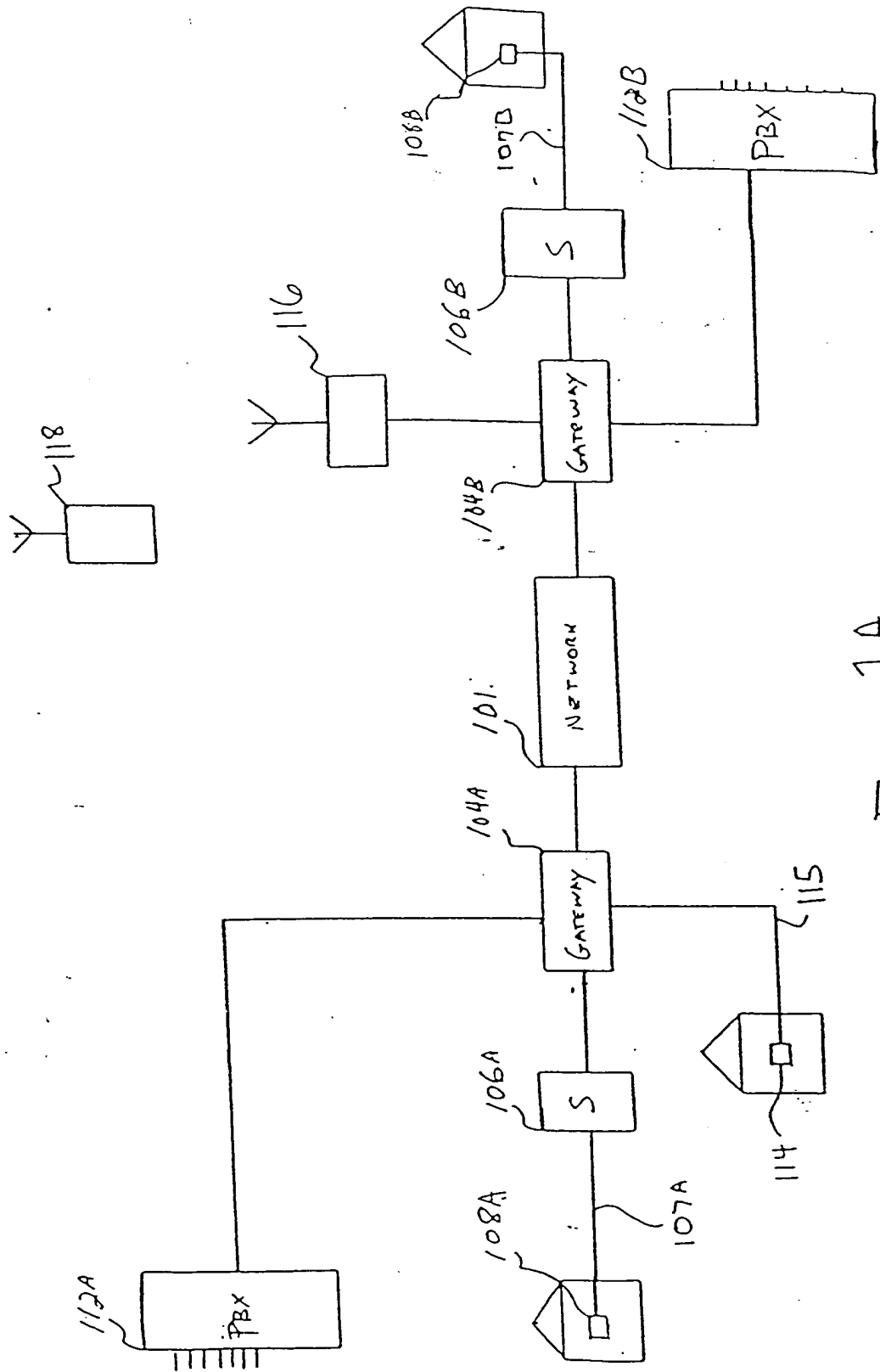
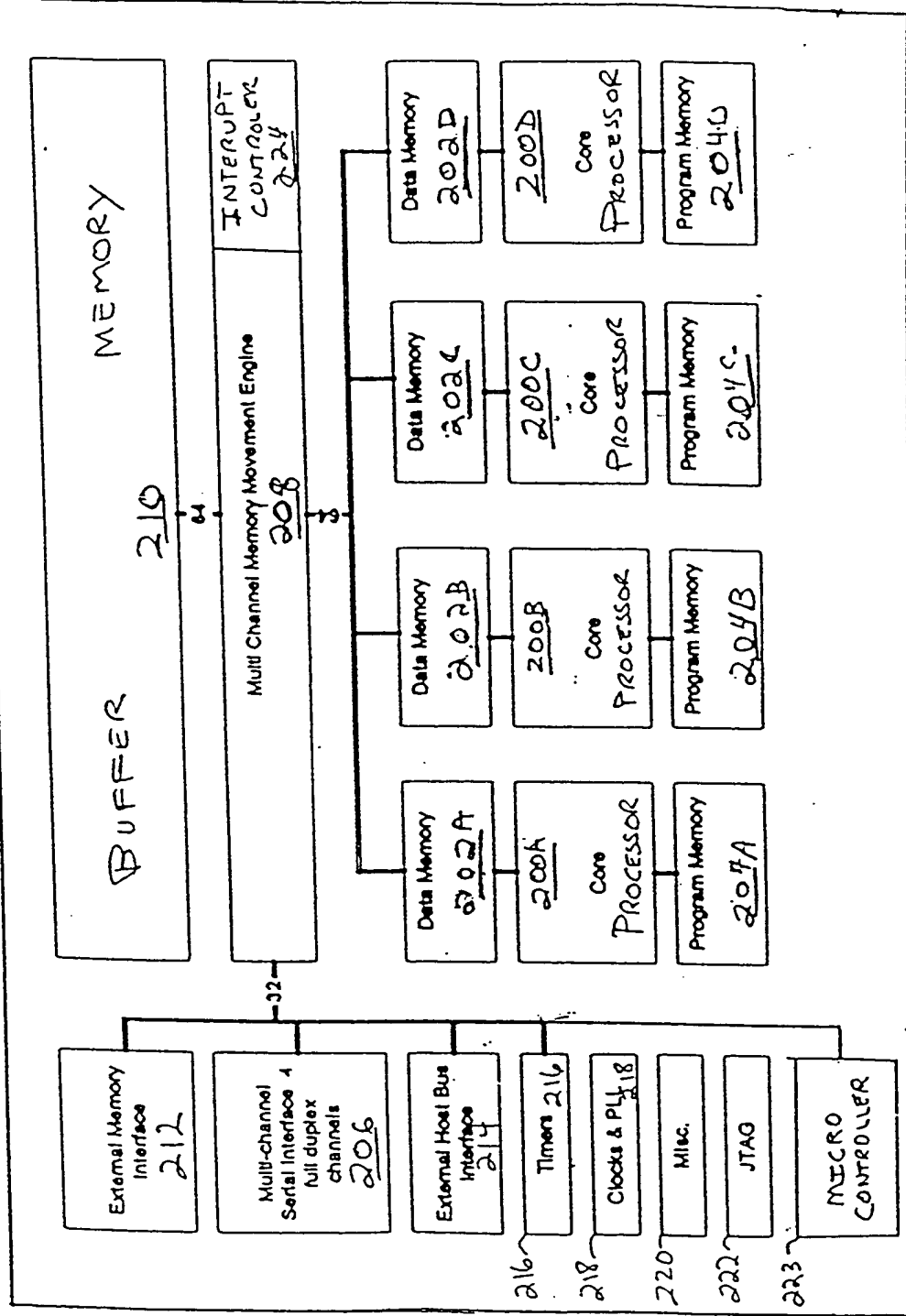


FIG. 1A



150



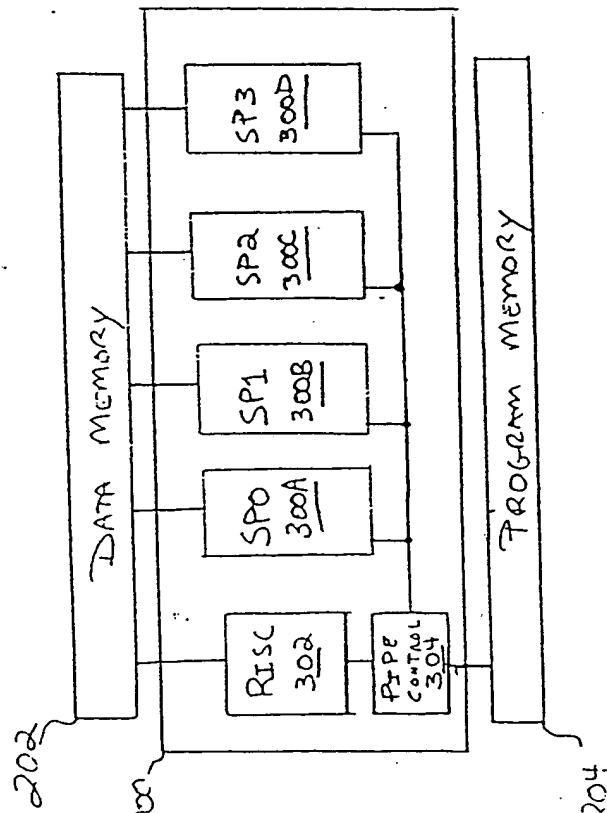


FIG. 3

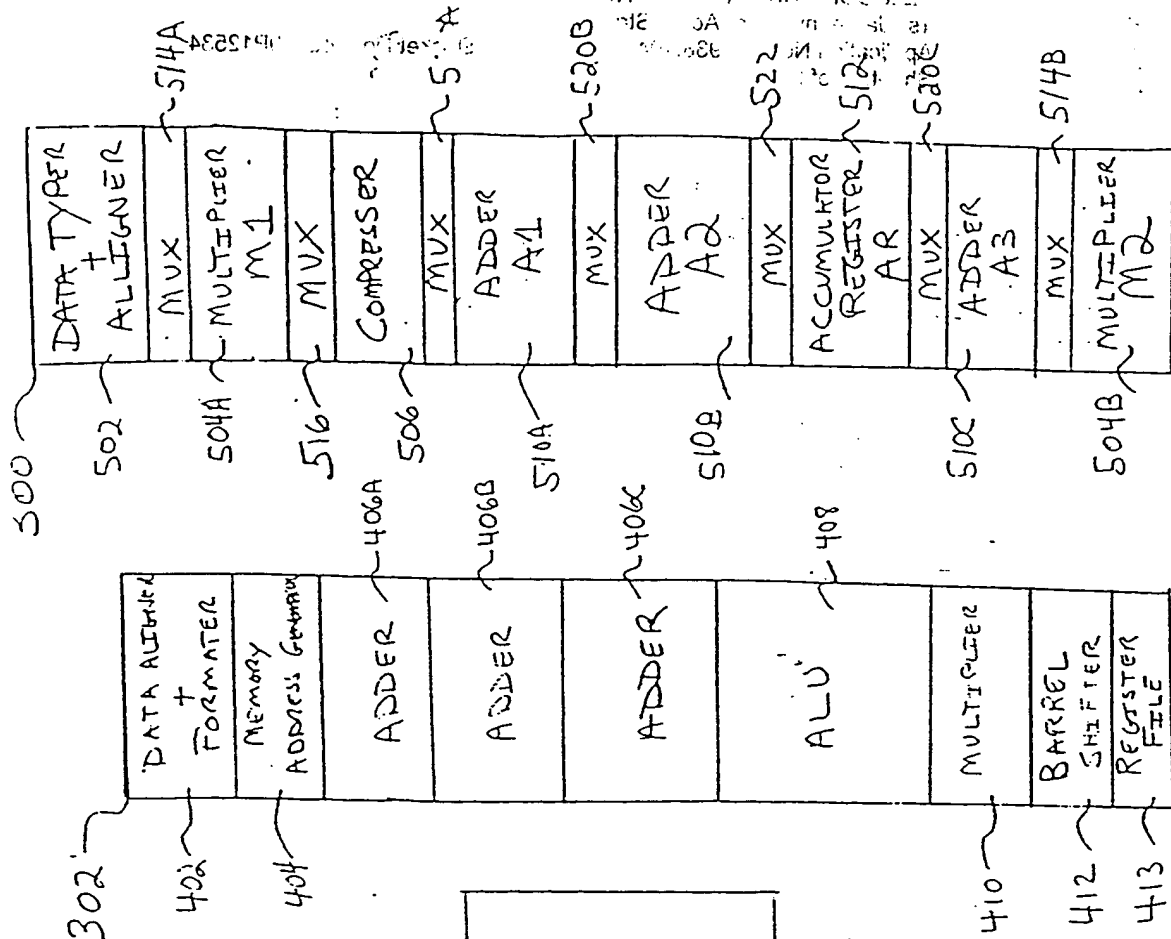


FIG. 4

FIG. 5A

$\begin{matrix} & X & Y & Z \\ 531 & \sim & | & \sim \\ & \downarrow & \downarrow & \uparrow \\ & & & 532 \end{matrix}$

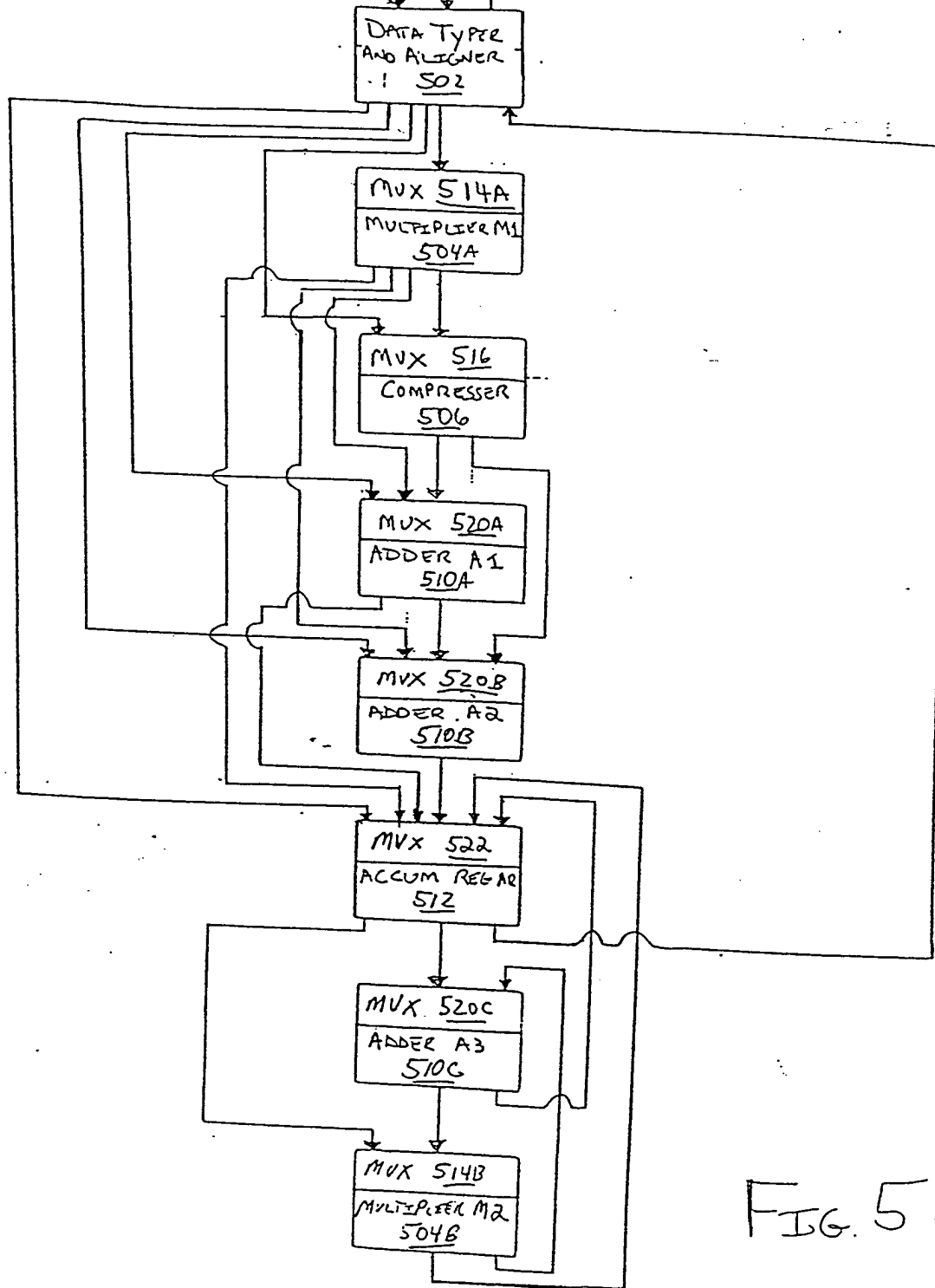
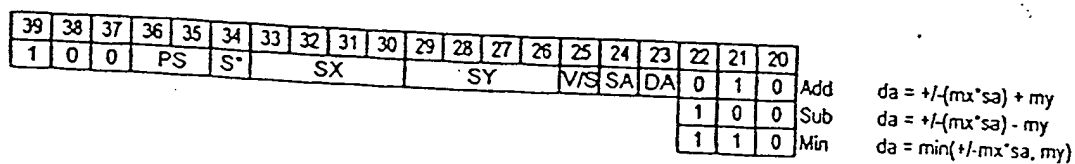
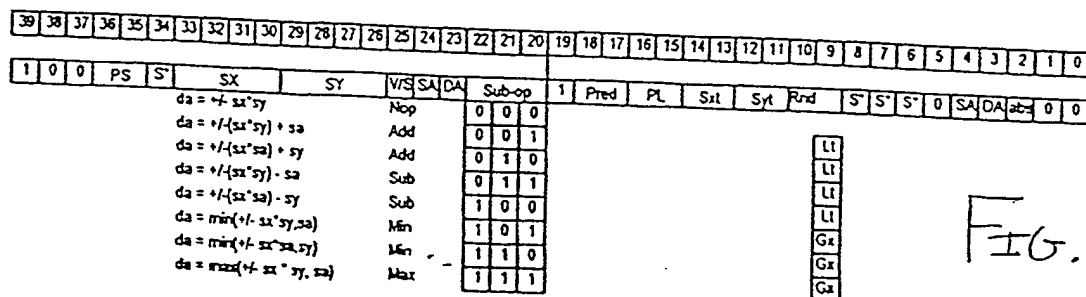
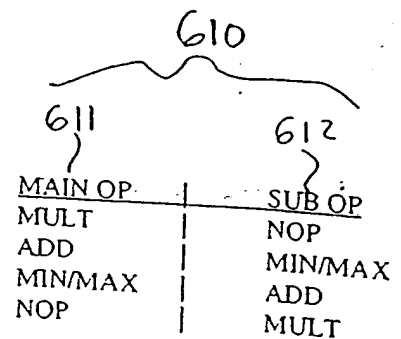


FIG. 5B



Control || Control  
Control # Control  
DSP, extensions/Shadow  
DSP # DSP

39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20
----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----

39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20						
1	0	0	PS	S*	SX	SY	V/S	SA	DA	Sub-op	Nop														
					$da = 1x \cdot 1y$				0	0	0														
					$da = (1x \cdot 1y) + 1a$				0	0	1	Add													
					$da = (1x \cdot 1a) + 1y$				0	0	1	Add													
					$da = (1x \cdot 1a) + 1a$				0	1	0	Sub													
					$da = (1x \cdot 1a) \cdot 1y$				0	1	0	Sub													
					$da = \min(1x \cdot 1y, 1a)$				1	0	1	Min													
					$da = \min(1x \cdot 1a, 1y)$				1	0	1	Min													
					$da = \max(1x \cdot 1y, 1a)$				1	1	0	Max													
1	0	1	PS	S*	SX	SY	V/S	SA	DA	Sub-op	Nop														
					$da = 1x + 1y$				0	0	0	1	Add												
					$da = 1x + 1y + 1a$				0	0	1	Add													
					$da = 1x + 1y + 1a + 1y$				0	1	0	AddSub													
					$da = (1x + 1y) \cdot 1a$				0	1	0	Mod													
					$da = (1x + 1y) \cdot 1a$				1	0	0	ModN													
					$da = \min(1x \cdot 1y, 1a)$				1	0	1	Min													
					$da = \max(1x \cdot 1y, 1a)$				1	1	0	Max													
					$da = \text{sum}(1x, 1y, 1a)$				1	1	0	CombAdd													
1	1	0	PS	S*	SX	SY	V/S	SA	DA	Sub-op	Nop														
					$da = \text{ext}(1x, 1y)$				0	0	0	1	Nop												
					$da = \text{ext}(1x, 1y, 1a)$				0	0	1	Ext													
					$da = \text{ext}(1x, 1a) \cdot 1y$				0	1	0	Mod													
					$da = \cdot \text{ext}(1x, 1a) \cdot 1y$				0	1	1	ModN													
					$da = \text{ext}(1x, 1a) + 1y$				1	0	0	Add													
					$da = \text{ext}(1x, 1a) + 1y$				1	0	1	Sub													
					$\text{ext}(1x, 1a) \cdot 1y + 1a$				1	1	0	amax													
1	1	0	PS																						
1	1	0	PS																						
1	1	1	PS																						
1	1	1	PS																						
						</																			

10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	-----

Address	min/max
00000000	00000000
00000001	00000001
00000002	00000002
00000003	00000003
00000004	00000004
00000005	00000005
00000006	00000006
00000007	00000007
00000008	00000008
00000009	00000009
0000000A	0000000A
0000000B	0000000B
0000000C	0000000C
0000000D	0000000D
0000000E	0000000E
0000000F	0000000F
00000010	00000010
00000011	00000011
00000012	00000012
00000013	00000013
00000014	00000014
00000015	00000015
00000016	00000016
00000017	00000017
00000018	00000018
00000019	00000019
0000001A	0000001A
0000001B	0000001B
0000001C	0000001C
0000001D	0000001D
0000001E	0000001E
0000001F	0000001F
00000020	00000020
00000021	00000021
00000022	00000022
00000023	00000023
00000024	00000024
00000025	00000025
00000026	00000026
00000027	00000027
00000028	00000028
00000029	00000029
0000002A	0000002A
0000002B	0000002B
0000002C	0000002C
0000002D	0000002D
0000002E	0000002E
0000002F	0000002F
00000030	00000030
00000031	00000031
00000032	00000032
00000033	00000033
00000034	00000034
00000035	00000035
00000036	00000036
00000037	00000037
00000038	00000038
00000039	00000039
0000003A	0000003A
0000003B	0000003B
0000003C	0000003C
0000003D	0000003D
0000003E	0000003E
0000003F	0000003F
00000040	00000040
00000041	00000041
00000042	00000042
00000043	00000043
00000044	00000044
00000045	00000045
00000046	00000046
00000047	00000047
00000048	00000048
00000049	00000049
0000004A	0000004A
0000004B	0000004B
0000004C	0000004C
0000004D	0000004D
0000004E	0000004E
0000004F	0000004F
00000050	00000050
00000051	00000051
00000052	00000052
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0000005A	0000005A
0000005B	0000005B
0000005C	0000005C
0000005D	0000005D
0000005E	0000005E
0000005F	0000005F
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00000068	00000068
00000069	00000069
0000006A	0000006A
0000006B	0000006B
0000006C	0000006C
0000006D	0000006D
0000006E	0000006E
0000006F	0000006F
00000070	00000070
00000071	00000071
00000072	00000072
00000073	00000073

Prop (uncl)  
1.1.17.14N  
1.1.17max.

1.1.1  
Add/sub ..

ce'

19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
----	----	----	----	----	----	----	----	----	----	---	---	---	---	---	---	---	---	---	---

11

0	00	PL	00	000	1	SA	DA	SUB-00											
19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

FIG. 6 E

Control Instructions

	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
add,sub	L	Pred	0	0	0	0	RX				RY					RZ			*/	0
max,min	L	Pred	0	0	0	0	RX				RY					RZ			X/N	1
Shift	L	Pred	0	0	0	0	RX				U4					RZ			U1/R1	1
Logic	L	Pred	0	0	0	0	RX				RY					RZ			Δ	1
Mux	L	Pred	0	0	0	0	RX				RY					RZ			Pd	0
mov	L	Pred	0	0	0	0	RX				OZ					RZ				0
addi	L	Pred	0	0	0	0	RX				OZ					RZ				0
mov2reg	L	Pred	0	0	0	0	RX				unil	ereg				RZ				0
Ldm	L	Pred	0	0	0	0	RX				DZ1					RZ				0
stbts	L	Pred	0	0	0	0	U14:POS				RZ					RZ			U14	0
bits	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Setbll	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Nov4	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Jump	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Call	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Loop	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Jmpi	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Call	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Loopl	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Test	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Testbll	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Andp,orp	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Load	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Store	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
eLoad	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
eStore	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Extended	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Logic2	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
mov-arg	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Cm	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Parity	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Sum	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Ads	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Nag	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
hw-itep	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
& Sat	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Return	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Zero-ac	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
eSync	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Sw	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0
Nop	L	Pred	0	0	0	0	U14:POS				RZ					RZ				0

<Bll1, Bll9-0> == U15 (Shift Amount)

<Bll3, Bll13-10> == U15:POS

FIG. 6 F

Checked by: 06-357-04  
 TELECOMMUNICATIONS IS PROCESSING  
 TIME: 14:00:00  
 Bldg: 1401 & 1402  
 (14) 8800





Group	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Period	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Order	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Subgroup	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Control	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Final	30	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

	PL	PS	Rnd	S*	DA	V/S	LI	S*	S*	S*
	PL	PS	Rnd	S*	DA	V/S	LI	S*		arg1
	PL	PS	Rnd	S*	DA	V/S	LI	S*	N/A	arg
	PL	PS	Rnd	S*	DA	V/S	LI	S*	S*	S*
	PL	PS	Rnd	S*	DA	V/S	LI	S*	S*	S*

[illegible]

Group	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
Group	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100

Group	Pred	Specs	Sx	Sy	Dz
37	36	35	34	33	32
38	37	36	35	34	33
39	38	37	36	35	34
40	39	38	37	36	35
41	40	39	38	37	36
42	41	40	39	38	37
43	42	41	40	39	38
44	43	42	41	40	39
45	44	43	42	41	40
46	45	44	43	42	41
47	46	45	44	43	42
48	47	46	45	44	43
49	48	47	46	45	44
50	49	48	47	46	45
51	50	49	48	47	46
52	51	50	49	48	47
53	52	51	50	49	48
54	53	52	51	50	49
55	54	53	52	51	50
56	55	54	53	52	51
57	56	55	54	53	52
58	57	56	55	54	53
59	58	57	56	55	54
60	59	58	57	56	55
61	60	59	58	57	56
62	61	60	59	58	57
63	62	61	60	59	58
64	63	62	61	60	59
65	64	63	62	61	60
66	65	64	63	62	61
67	66	65	64	63	62
68	67	66	65	64	63
69	68	67	66	65	64
70	69	68	67	66	65
71	70	69	68	67	66
72	71	70	69	68	67
73	72	71	70	69	68
74	73	72	71	70	69
75	74	73	72	71	70
76	75	74	73	72	71
77	76	75	74	73	72
78	77	76	75	74	73
79	78	77	76	75	74
80	79	78	77	76	75
81	80	79	78	77	76
82	81	80	79	78	77
83	82	81	80	79	78
84	83	82	81	80	79
85	84	83	82	81	80
86	85	84	83	82	81
87	86	85	84	83	82
88	87	86	85	84	83
89	88	87	86	85	84
90	89	88	87	86	85
91	90	89	88	87	86
92	91	90	89	88	87
93	92	91	90	89	88
94	93	92	91	90	89
95	94	93	92	91	90
96	95	94	93	92	91
97	96	95	94	93	92
98	97	96	95	94	93
99	98	97	96	95	94
100	99	98	97	96	95

Year	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127	128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159	160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191	192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223	224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255	256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287	288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319	320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351	352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383	384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447	448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466	467	468	469	470	471	472	473	474	475	476	477	478	479	480	481	482	483	484	485	486	487	488	489	490	491	492	493	494	495	496	497	498	499	500	501	502	503	504	505	506	507	508	509	510	511	512	513	514	515	516	517	518	519	520	521	522	523	524	525	526	527	528	529	530	531	532	533	534	535	536	537	538	539</
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[illegible]

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23	34	32	31	30	29	28
24	33	31	30	29	28	27
25	32	30	29	28	27	26
26	31	29	28	27	26	25
27	30	28	27	26	25	24
28	29	27	26	25	24	23
29	28	26	25	24	23	22
30	27	25	24	23	22	21
31	26	24	23	22	21	20
32	25	23	22	21	20	19
33	24	22	21	20	19	18
34	23	21	20	19	18	17
35	22	20	19	18	17	16
36	21	19	18	17	16	15
37	20	18	17	16	15	14
38	19	17	16	15	14	13
39	18	16	15	14	13	12
40	17	15	14	13	12	11
41	16	14	13	12	11	10
42	15	13	12	11	10	9
43	14	12	11	10	9	8
44	13	11	10	9	8	7
45	12	10	9	8	7	6
46	11	9	8	7	6	5
47	10	8	7	6	5	4
48	9	7	6	5	4	3
49	8	6	5	4	3	2
50	7	5	4	3	2	1
51	6	4	3	2	1	0

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Sheet 3 of 32  
Application No. 10918  
Inventive in London And Stras  
TELECOMMUNICATIONS PROCESSING  
THE VOICE ACTIVITY DETECTOR FOR INTEGRATED  
Bisley, Seaton, Taylor & Zaiman LLP  
(714) 887-3871



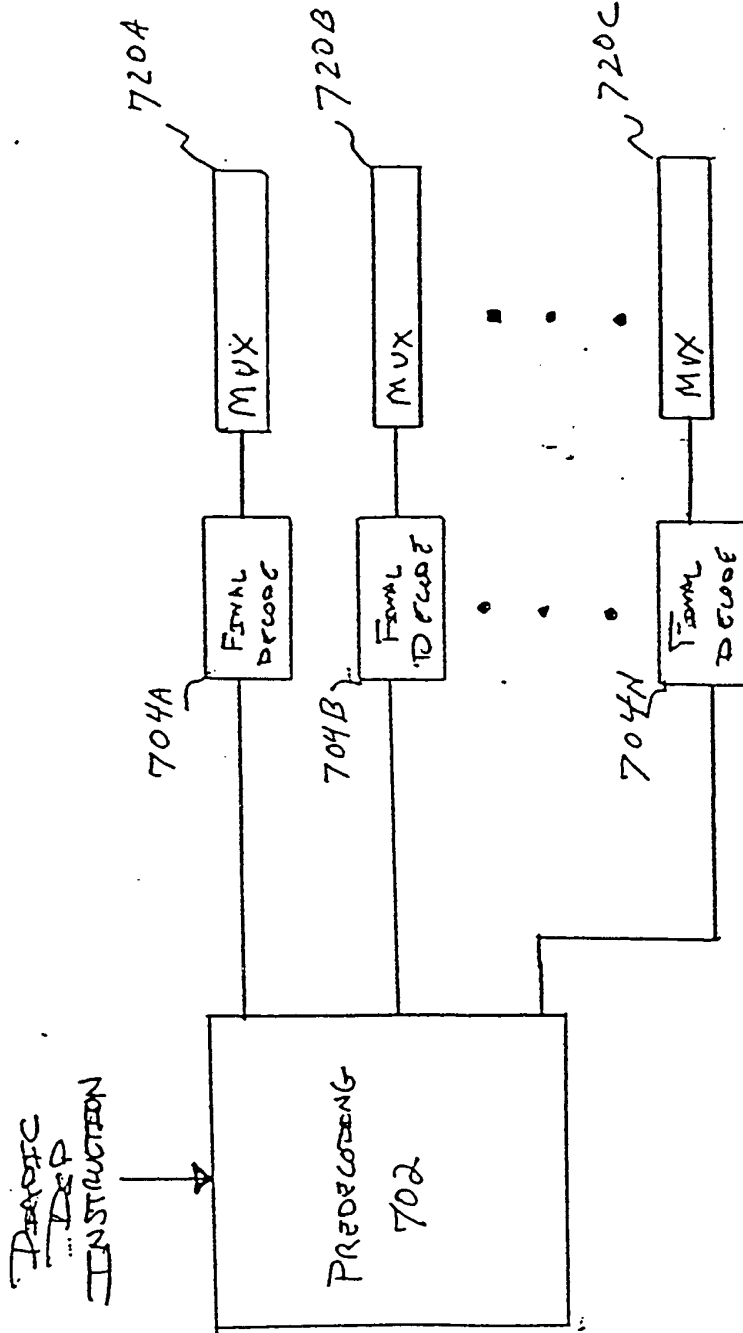


FIG. 7

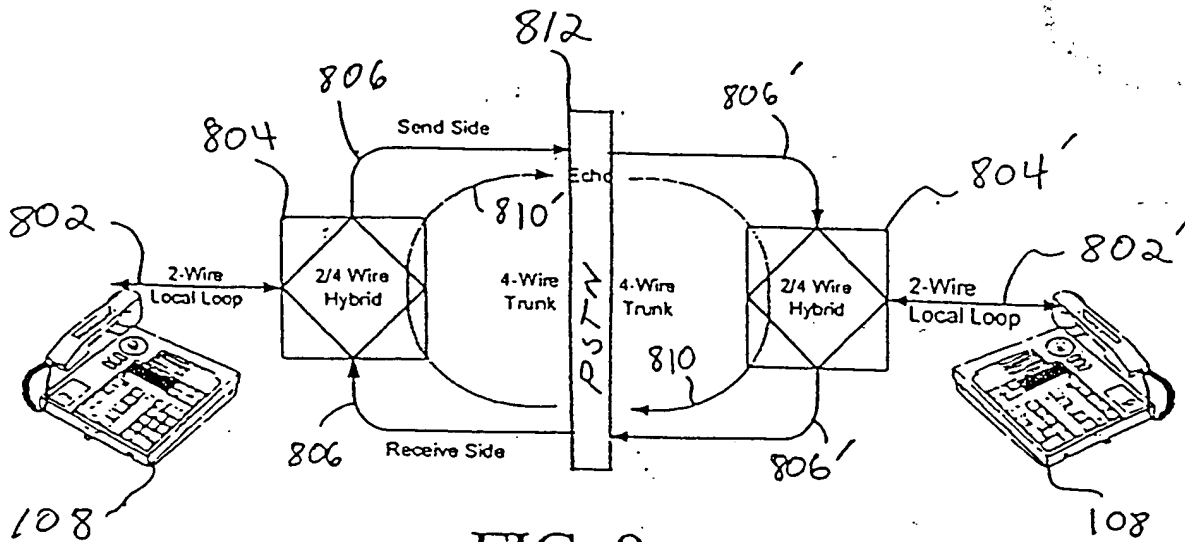


FIG. 8  
(PRIOR ART)

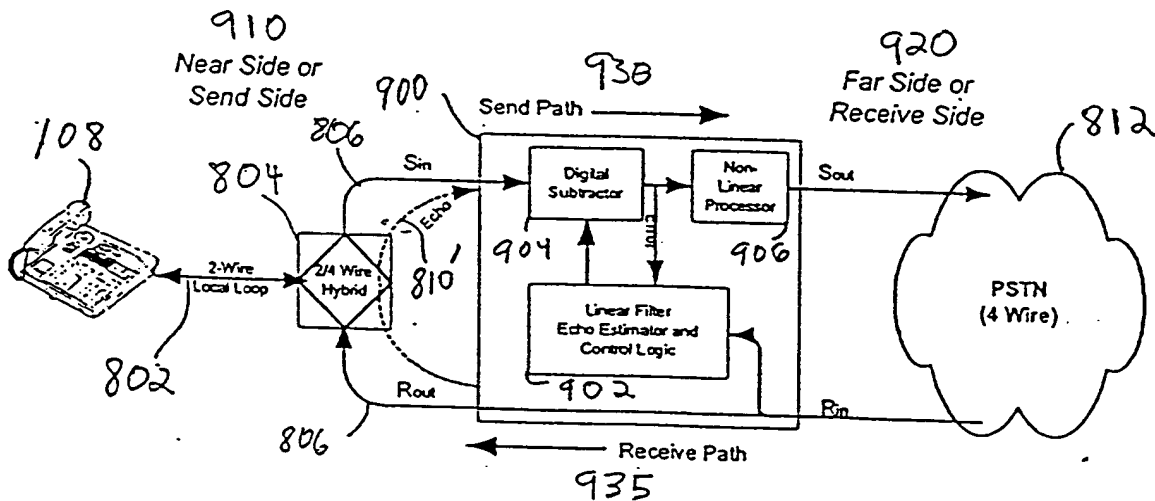


FIG. 9  
(PRIOR ART)

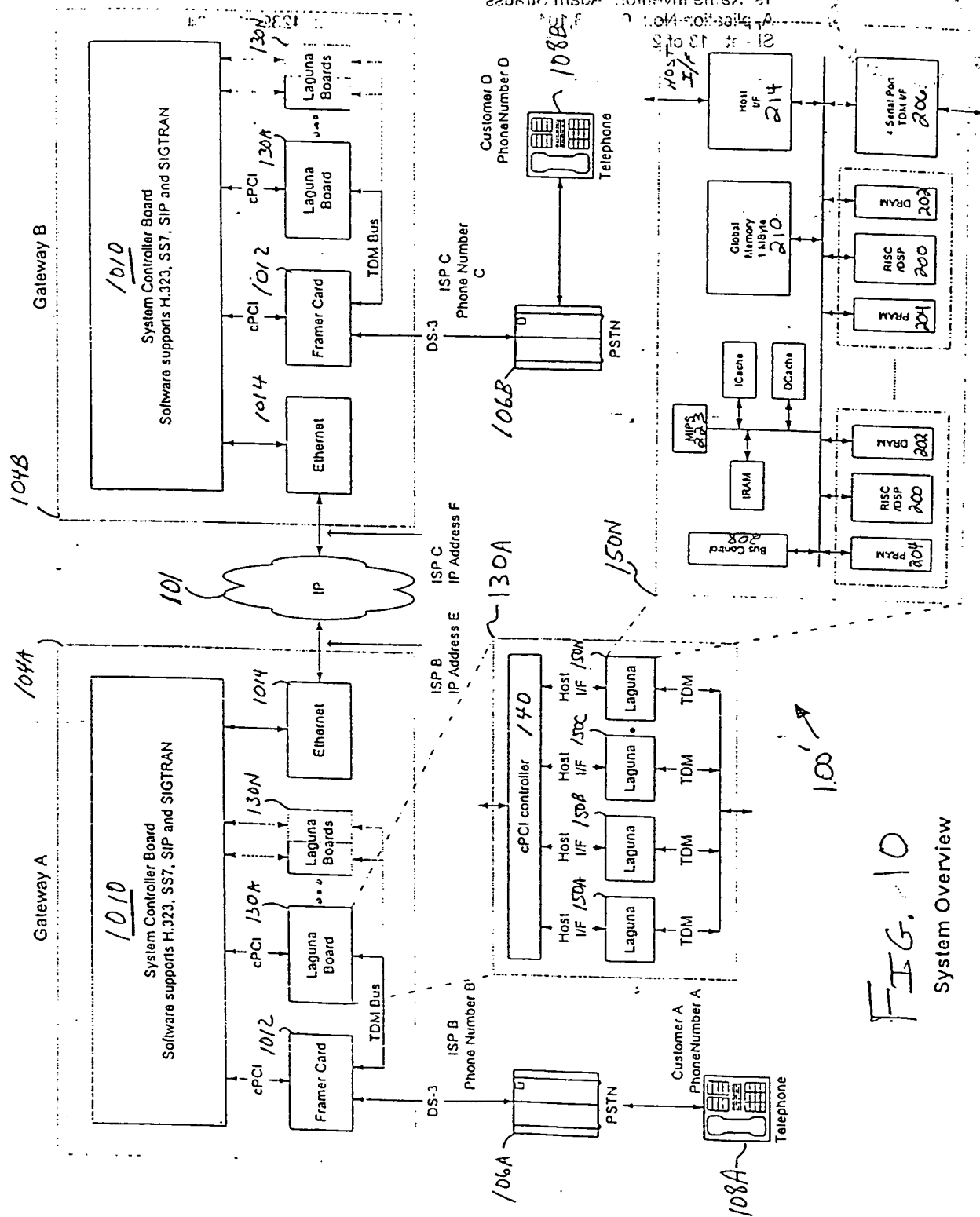
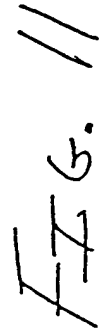


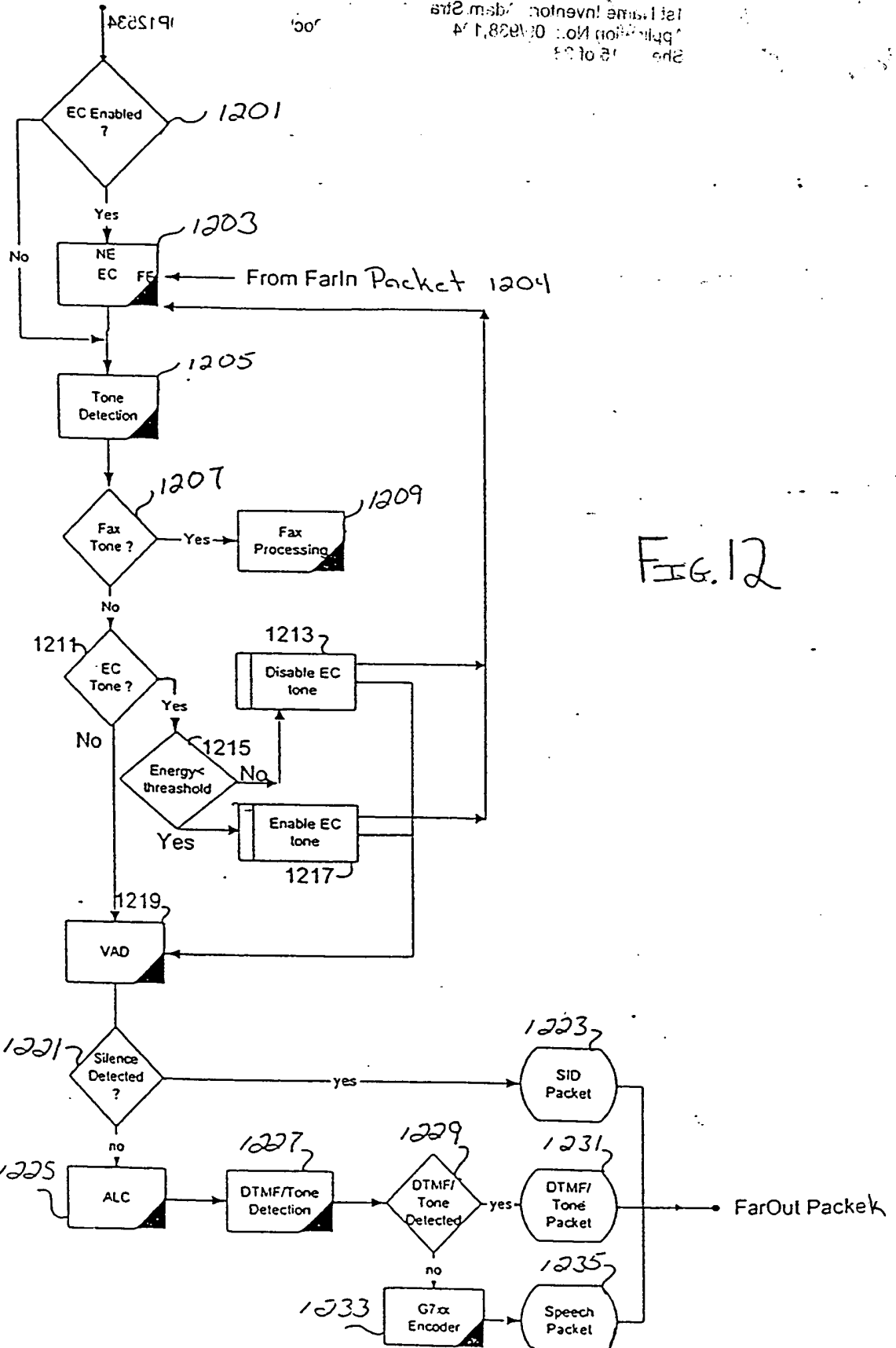
FIG. 10

(end)  
New York  
Admission No. 9836-10  
Listed in the Adam's  
Telephone Communications Reporting  
Third Voice Activity Defendant  
Biskamp, Sokoloff, Tavor & Zelman LLP  
(714) 581-3821



116

Near In TDI



(714) 527-3800

Blakely, Sokoloff, Taylor & Zisman LLP  
TEL: 714 527-3800  
FAX: 714 527-3801  
www.blakely.com

10/10/2001 10:10:10



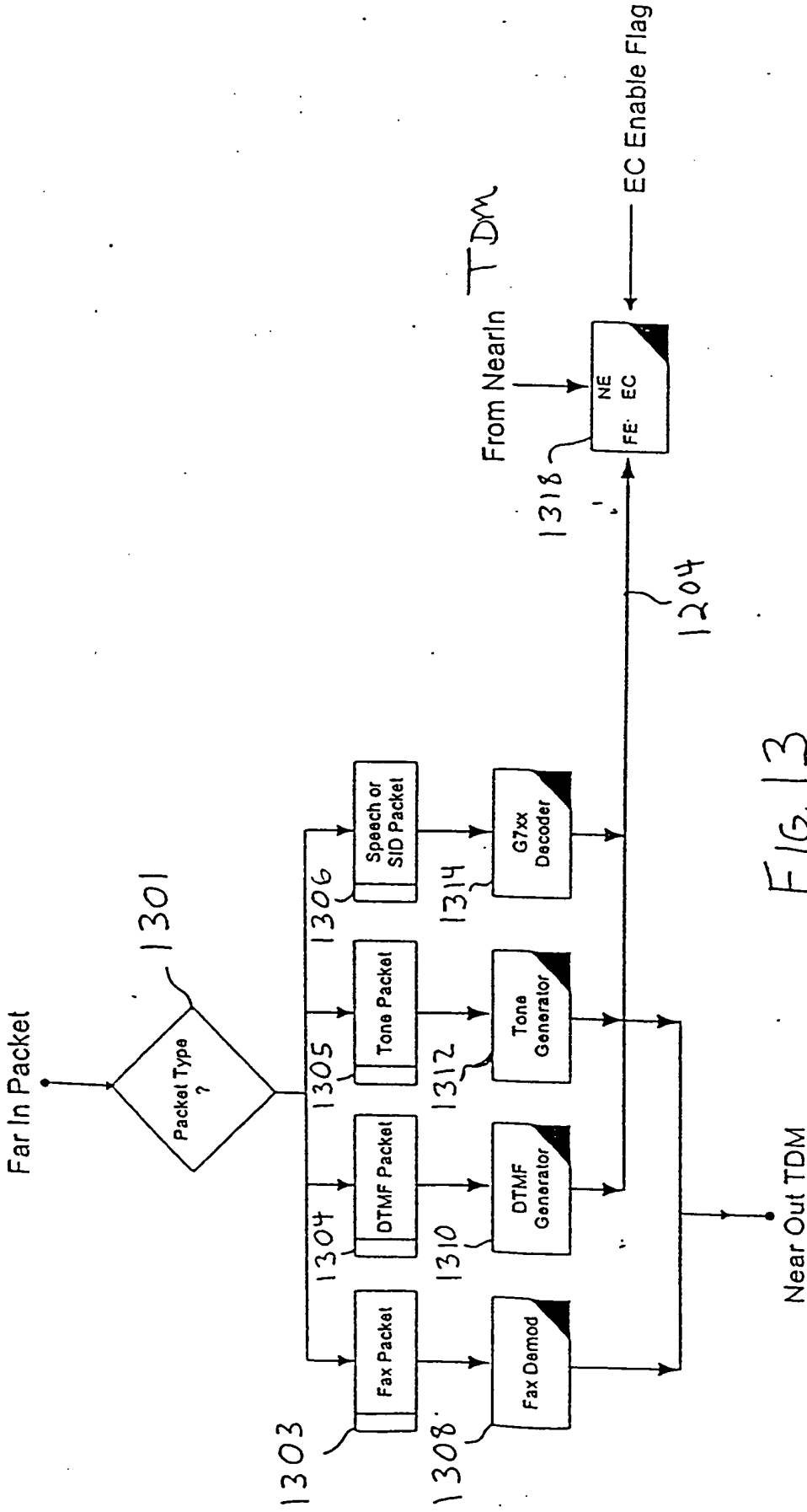


FIG. 13

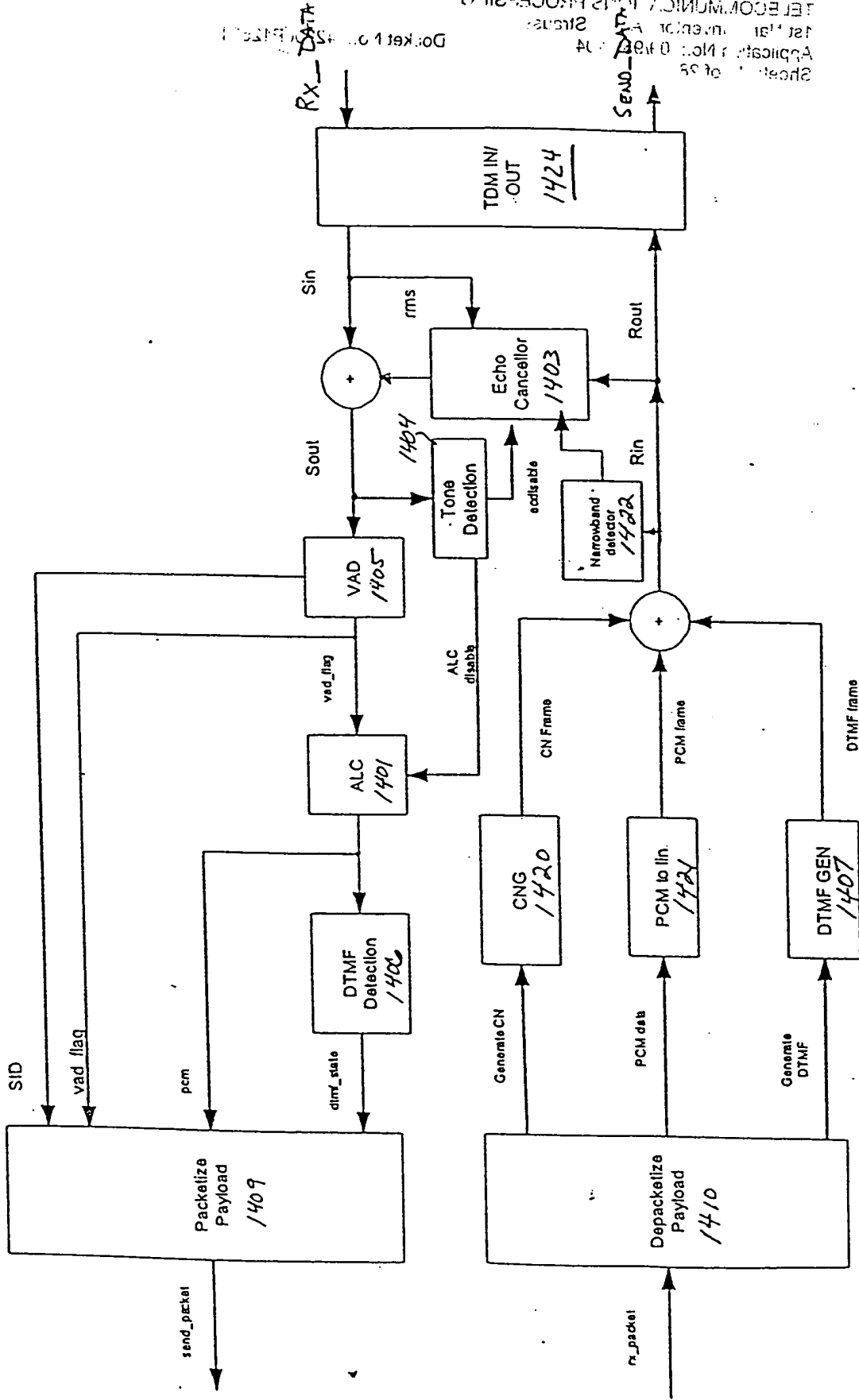


FIG. 14A

Sheet 1 of 2  
Applicant: Motorola, Inc.  
Inventor: Andrew J. Street  
TELECOMMUNICATIONS PROCESSING  
VOICE ACTIVITY DETECTOR FOR DECODED  
Trellis Softswitch Taylor & Zeffman LLP  
(274) 581-1100

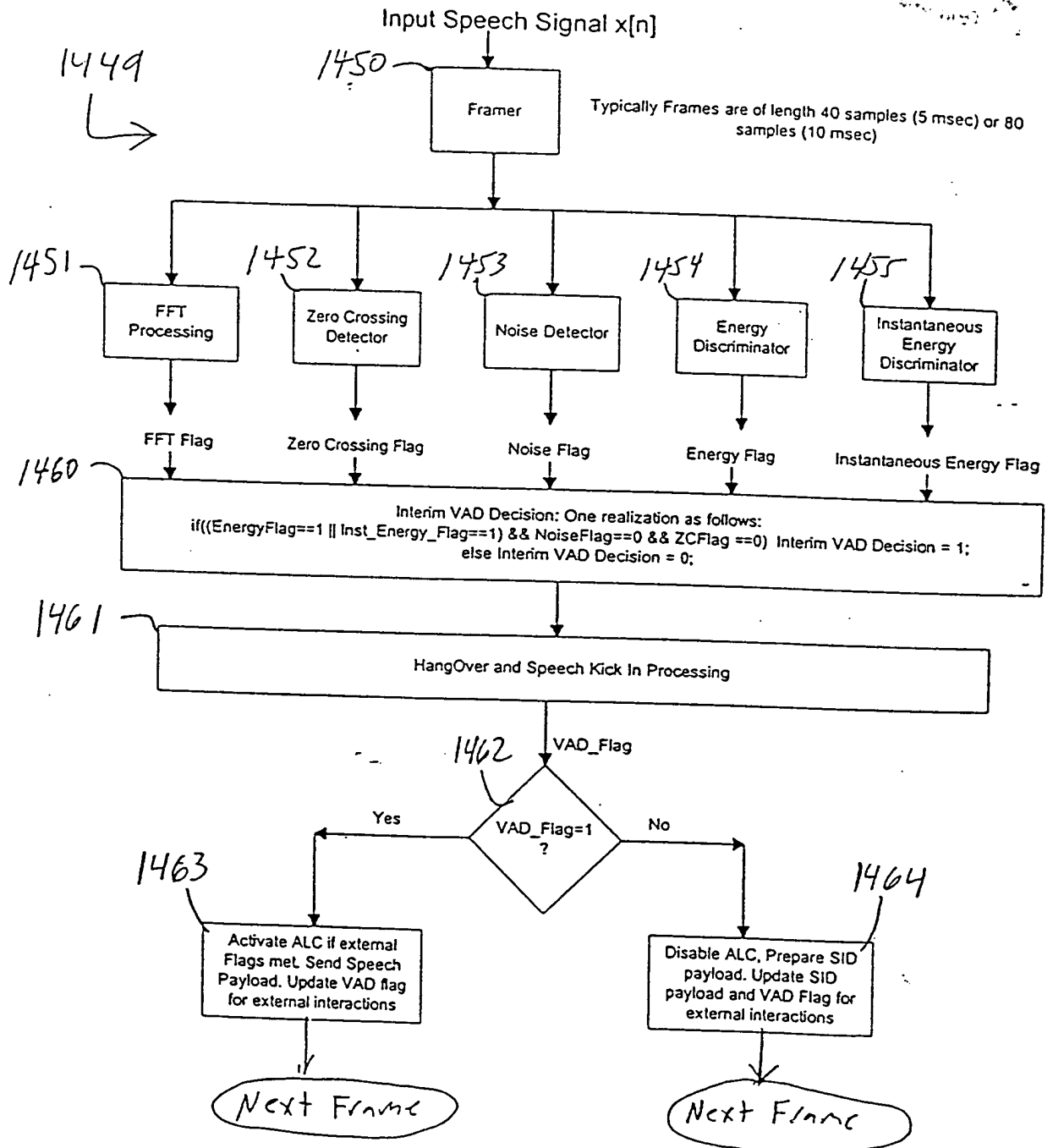
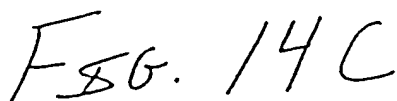
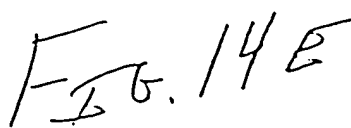


FIG. 14B

1451



## Zero Crossing Detector



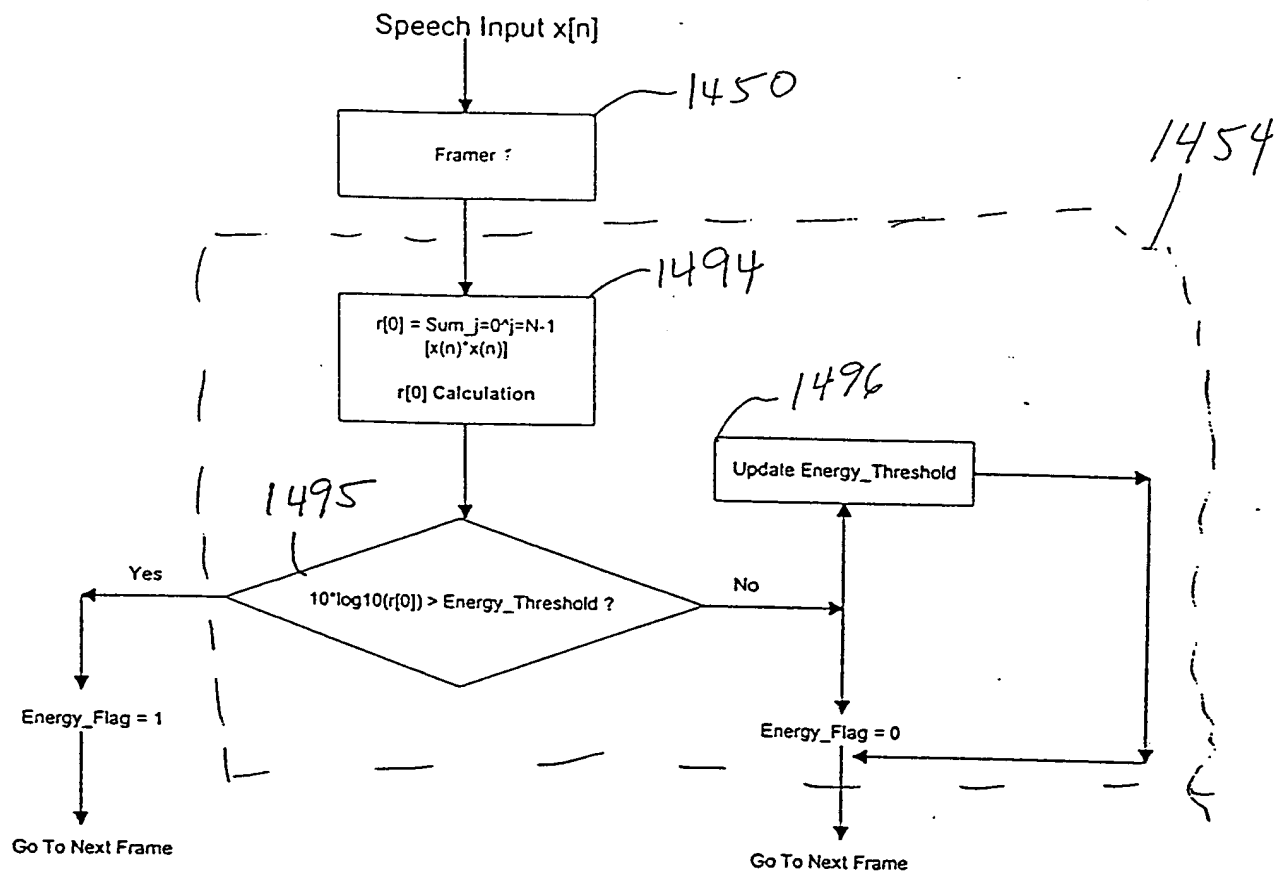
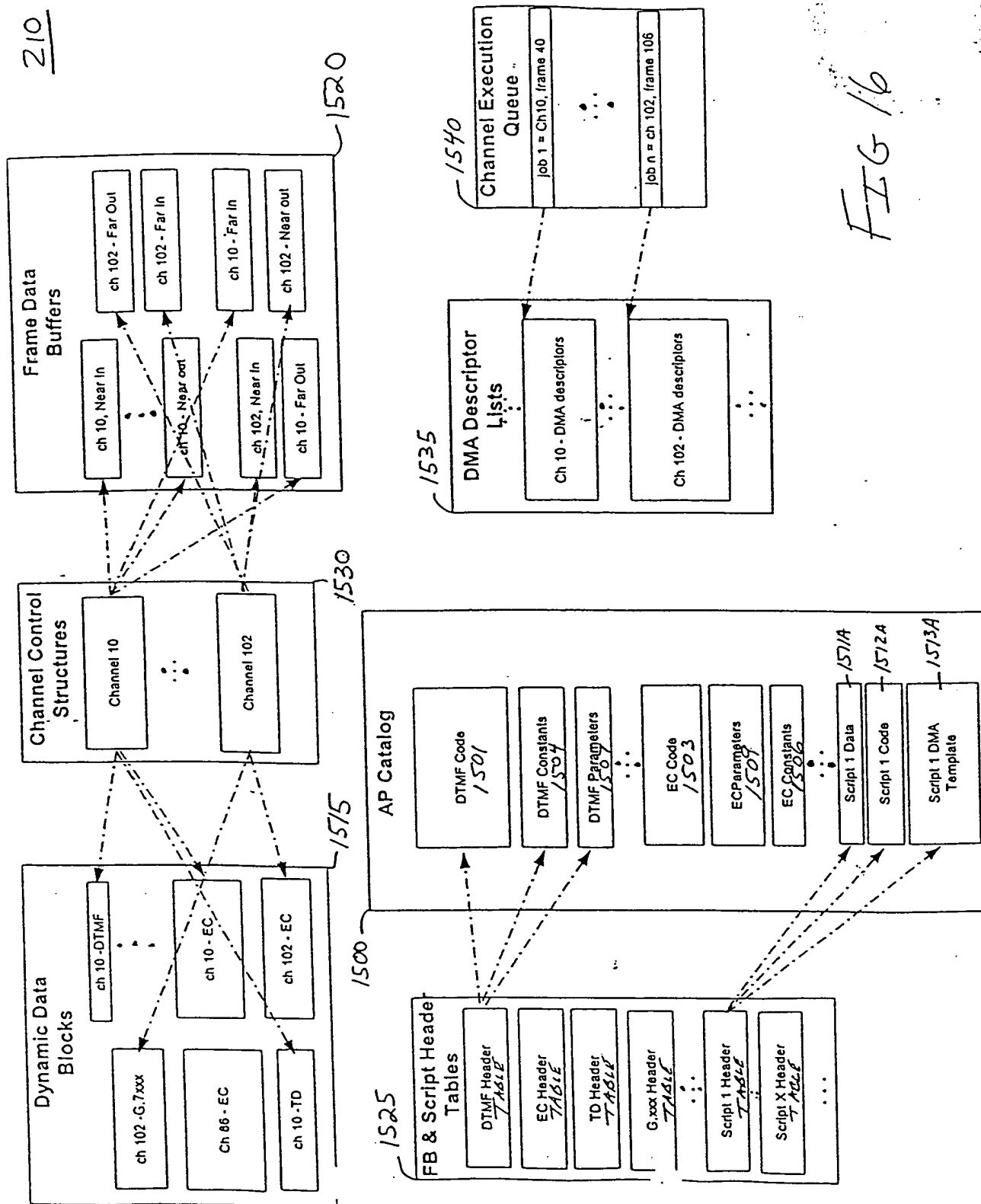


FIG. 14F









FOOTNOTES

03/03/2014  
Patent Invention: Adam Staines  
TEL COMMUNICATIONS PROCESSING  
TIME VOICE ACTIVITY DETECTOR FOR INTEGRATED  
Biskely, Sokoloff, Taylor & Zaitoun LLP  
1714/25-3900

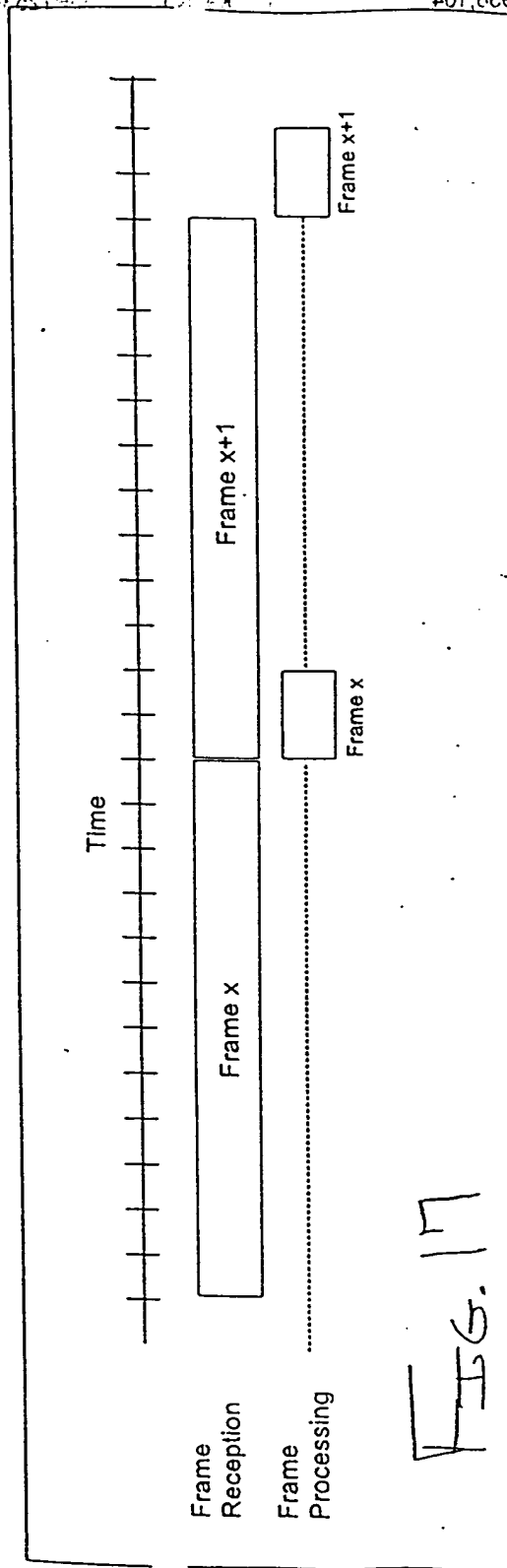


FIG. 17

Time (arbitrary units)

